EV318279898

Inventor:

Kunal R. Parekh et al.

Title:

Methods of Forming Memory Circuitry

Assignee: Micron Technology, Inc.

INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: <u>7-7-03</u>

Attorney

Mark S. Matkin Reg. No. 32,268

EV318279898

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. M122-2260

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LIST OF ART CITED BY APPLICANT

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APPLICANT: Kunal R. Parekh et al.

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	АВ	5,563,089	10/08/96	Jost et al.					
	AC	5,686,747	11/11/97	Jost et al.					
	AD	5,821,140	10/13/98	Jost et al.			<u> </u>		
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APPLICANT: Kunal R. Parekh et al.

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APPLICANT: Kunal R. Parekh et al.

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